

DSR8GD4-32

8GB (1G x 64-Bit.)

DDR4 3200MHz CL 22 260-Pin DIMM



SPECIFICATIONS

CL(IDD)	22 cycles
Row Cycle Time (tRCmin)	44.45ns (min.)
Refresh to Active/Refresh Command Time (tRFCmin)	350ns (min.)
Row Active Time (tRASmin)	32ns (min.)
Maximum Operating Power	TBD W*
UL Rating	94 V - 0
Operating Temperature	0°C to +85°C
Storage Temperature	-55°C to +100°C

*Power will vary depending on the SDRAM used.

DESCRIPTION

This document describes BitMem 1G x 64bit, 8GB DDR4-3200 CL22, Synchronous Dynamic Random Access Memory Module (SDRAM) 1Rx8, memory module, based on eight 1G x 8-bit FBGA components per module. Each module has been tested to run at DDR4-3200. The SPDs are programmed to JEDEC standard latency DDR4-3200 (PC4-25600) timing of 22-22-22 at 1.2V. Each 260-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

FEATURES

- ❖ Power Supply: VDD = 1.2V Typical
- ❖ VDDQ = 1.2V Typical
- ❖ VPP - 2.5V Typical
- ❖ VDDSPD = 2.25V to 3.6V
- ❖ On-Die termination (ODT)
- ❖ 16 internal banks; 4 groups of 4 banks each
- ❖ Bi-Directional Differential Data Strobe
- ❖ 8 bit pre-fetch
- ❖ Burst Length (BL) switch on-the-fly BL8 or BC4 (Burst Chop)
- ❖ Weight 34 gram
- ❖ Dimensions 133.35 * 33.6 * 6.8 mm

NOTE:

The product may vary with minor technical specifications within limits as defined by JEDEC standards due to continuous technology upgrades. Company holds the right to change product specifications without prior notice.